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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/478,916	01/06/2000	JARED L. ZERBE	9797-050-999	2374
38426	7590 03/21/2005		EXAMINER	
MORGAN LEWIS & BOCKIUS LLP/RAMBUS INC.			VO, TIM T	
2 PALO ALTO SQUARE 3000 EL CAMINO REAL		ART UNIT	PAPER NUMBER	
PALO ALTO, CA 94306			2112	
		DATE MAILED: 03/21/2005		5

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
Office Action Summary		09/478,916	ZERBE ET AL.			
		Examiner	Art Unit			
		Tim T. Vo	2112			
Period fo	The MAILING DATE of this communication a or Reply	appears on the cover sheet with the	correspondence address			
A SH THE - Exte after - If the - If NC - Failt Any	ORTENED STATUTORY PERIOD FOR REF MAILING DATE OF THIS COMMUNICATION nsions of time may be available under the provisions of 37 CFR SIX (6) MONTHS from the mailing date of this communication. Period for reply specified above is less than thirty (30) days, a roperiod for reply is specified above, the maximum statutory perior to reply within the set or extended period for reply will, by stated the period for reply will, by stated for reply will, by stated for the period for reply will, by stated f	N. 1.136(a). In no event, however, may a reply be ti reply within the statutory minimum of thirty (30) da od will apply and will expire SIX (6) MONTHS fron tute, cause the application to become ABANDONI	mely filed ys will be considered timely. n the mailing date of this communication. ED (35 U.S.C. § 133).			
Status			• •			
1)🖂	Responsive to communication(s) filed on 14	January 2005.				
2a)⊠		his action is non-final.				
3)□	, 					
Disposit	ion of Claims		·			
_	Claim(s) <u>126-140 and 142-160</u> is/are rejected. Claim(s) <u>141</u> is/are objected to.					
Applicat	ion Papers					
9)[The specification is objected to by the Exami	iner.				
10)	10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.					
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
11)[Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 1) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.					
Priority (under 35 U.S.C. § 119					
a)	Acknowledgment is made of a claim for forei All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the priority docume application from the International Bure See the attached detailed Office action for a light	ents have been received. ents have been received in Application of the properties of	tion No red in this National Stage			
Attachmen	• •	_				
	ee of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948)	4) 🔲 Interview Summar Paper No(s)/Mail D				
3) 🔲 Infor	te of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/0 or No(s)/Mail Date		Patent Application (PTO-152)			

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Part III DETAILED ACTION

Notice to Applicant(s)

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1. This application has been examined. Claims 126-160 are pending.

Double Patenting

- 2. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970);and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).
- 3. Claims 1-42 of patent 6,396,329 contains every element of claims 126-160 of instant application and as such anticipates claims 126-160 of the instant application. Therefore, claims 126-160 of the instant application is not patently distinct from the earlier patent claims and as such is unpatentable for obvious-type double patenting.
- 4. "A later patent claim is not patentably distinct from an earlier patent claim if the later claim is obvious over, or anticipated by, the earlier claim. In re Longi, 759 F.2d. at 896, 225 USPQ at 651 (affirming a holding of obviousness-type double patenting because the claims at issue were obvious over claims in four prior art patents); In re Berg, 140 F.3d at 1437, 46 USPQ2d at 1233 (Fed. Cir. 1998) (affirming a holding of obviousness-type double patenting where a patent application claim to a genus is

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anticipated by a patent claim to a species within that genus)". ELI LILLY AND COMPANY v BARR LABORATORIES, INC., United States Court of Appeals for the Federal Circuit, ON PETITION FOR REHEARING EN BANC (DECIDED: MAY 30, 2001).

5. A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 6. Claims 126-140 and 142-160 are rejected under 35 U.S.C. § **102(b)** as being anticipated by Hui patent number 5,793,816.

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As for claim 126 and 142, Hui teaches a multi-mode PAM output receiver for receiving a symbol (see figure 5, receiver 109, inputs X, Y, wherein the receiver 109 receives inputs symbol X, Y) the receiver comprising:

A receiver unit configured to receive the symbol (see figure 5, receiver 109, inputs X, Y, wherein the receiver 109 receives inputs symbol X, Y); and

Logic circuitry coupled to the receiver circuit (see figure 5, 307, 309 and column 5 lines 50-67);

Wherein the multi-mode PAM receiver is capable of operating in a specified mode of a plurality of predefined modes (see column 1 line 47 to column 2 line 14);

Wherein the received symbol is an N-PAM symbol when the specified mode is a first mode (see figure 5, input X is assigned to 1 of 4 levels of voltage signal values as disclosed in column 1 lines 47-65);

Wherein the received symbol is an M-PAM symbol when the specified mode is a second mode (see figure 5, input Y is assigned to 1 of 4 levels of voltage signal values as disclosed in column 1 lines 47-65); and

Wherein N is not equal to M (see figure 5, X, Y).

As for claims 127-131, 149-151, Hui teaches wherein the receiver outputs one or more binary signals synchronized to a clock signal (see figure 5, output P01, P02 and column 3 table A-B).

As for claim 132, Hui teaches the receiver circuit latches the symbol at a symbol rate selected from a plurality of predefined symbol rate (see figure 5).

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As for claims 133, 140, Hui teaches the specified mode is a second mode, the receive symbol represents two or more bits including a most significant bit (MSB) and at least significant bit (LSB) (see figure 5, inputs A, B, X, Y represent binary bits as disclosed in table A-B).

As for claims 134-139, 146-148 and 156-160, Hui teaches wherein the received symbol represents at least a part of an address, a value, a part of a command in a memory system (see figure 5, input symbols A, B, X, Y).

As for claims 143-145, Hui teaches the multi-mode PAM output driver and multi-mode PAM receiver are embodied on a single integrated circuit (see figure 5).

As for claims 152-153, Hui teaches the symbol rate is determined by control logic, and the control logic determines the state of the mode signal based, at least in part, on the second stream signals (see figure 5, logic circuits 301, 303, 305).

As for claims 154-155, Hui teaches the symbol rate is selected from amongst a plurality of distinct symbol rates (see figure 5, tables A-B).

Allowable Subject Matter

7. Claims 141 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Examiner's Statement of Reasons for Allowance

8. The following is an Examiner's statement of reasons for the indication of allowable subject matter: Claim 141 is allowable over the prior art of record because

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the Examiner found neither prior art cited in its entirety, nor based on the prior art, found any motivation to combine any of the said prior arts. As for claim 141, prior art fails to teach the second receiver circuit includes a second LSB latching comparator to compare the input symbol to a third reference voltage and to generate a third signal representing a relationship between the symbol and the third reference voltage.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Response to Arguments

- 9. Applicant's arguments filed 1/14/05 have been fully considered but they are not persuasive.
- 10. In response to the applicant's arguments that currently pending claims are not non-obvious from the Zerbe. See ELI LILLY AND COMPANY v BARR LABORATORIES, INC as shown above.
- 11. In response to the applicant's arguments that Hui teaches one mode of operation i.e. outputs voltage levels selected from a set of four voltages levels and referred to the office action "input Y is assigned to 1 of 4 levels". The claim's language requires the multimode PAM receiver is capable of operating in a specified mode of predefined modes i.e. a first mode when it receives N-PAM symbol and second mode when it receives M-PAM symbol. Hui teaches a transceiver receiving X and Y input symbols and when the transceiver receives a X symbol it operates in 1 of the 4 voltage levels

and when the transceiver receives a Y symbol it operates in 1 of the 4 voltage levels. Wherein the 1 to 4 voltage levels are various modes, which constitutes as being first, second, third and fourth mode. This teaching is equivalent to what is claimed i.e. the transceiver operates in 1 of the 4 voltage levels when it receives X symbol, say selects 1 voltage level which equivalent to a specified mode, thus transceiver receives a Y symbol and assigns to 1, 2, 3 and 4 voltage level i.e. a second mode, wherein each voltage level is interpreted as a specified mode.

Conclusion

12. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tim T. Vo whose telephone number is 571-272-3642. The examiner can normally be reached on 7:30-5:00.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark Rinehart can be reached on 571-272-3632. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free)

3/16/05

Tim T. Vo

Primary Examiner
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